

Ahmad Gol-mohammadi

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Education

- **M.Sc. in Computer Engineering**
Sharif University of Technology (SUT), Tehran, Iran.
Thesis: Design and Implementation of a Processor for Adaptive IIR filters.
September 1998 – December 2000
- **B.Sc. in Computer Engineering**
Shahid Beheshti University (SBU), Tehran, Iran.
Thesis: Design and Implementation of an EPROM Emulator.
September 1992 – November 1997

Research Interests

- Digital Signal Processing, Digital Communication, Image Processing, Speech Processing.

Honors

- Ranked 3th in SBU.
- Ranked 32th in Nationwide Competition for Graduate Studies among 3000 Students.

Experience

- **Electronic Research Center, Sharif University of Technology, Tehran, Iran**
September 2004 - Present
I. Research Assistant, VOIP (Voice over IP) project, Implementation of G.726, G.722 and G.723 ADPCM (Adaptive Differential Pulse Code Modulation) codec, and G.711 PCM codec using C54x assembly. (*September 2004 – September 2005*)
II. Design and Implementation of MAC layer of 802.11. (*September 2005 - Present*)
- **Eimaa Communication Technologies Co. Ltd., Tehran, Iran**
August 2003 - September 2004
Senior Software Architect and Designer, MMS (Multimedia Messaging System) for Mobile Networks, Design and Implementation of MMS Transport Layer and MMS Relay Server Emulator for Testing MMS Based Mobile Systems (Visual C++).

- **Iran Telecommunication Research Center (ITRC), Tehran, Iran
October 2002 - August 2003**
Research Assistant, Zohreh Satellite national project, Design and implementation of slotted Aloha, TDM, and polling connection methods in MAC Layer using a TMS320C54x DSP and a Xilinx FPGA.
- **Electronic Research Center, Sharif University of Technology, Tehran, Iran
June 2001 - October 2002**
Research Assistant, WLL (Wireless Local Loop) project Design of PCB boards, Hardware design of ISA PC cards, Programming of Altera FPGAs, Research on clock and data recovery algorithms in a wireless system, Developing device drivers in C++ codes.

Publications

- **Ahmad Golmohammadi**, M.T. Manzuri, Saeed Ayat, "*A New Pipeline Implementation of an Adaptive IIR Filter for Noise Reduction Application*," IEEE International Symposium on Communications and Information Technologies 2004 (ISCIT 2004), Sapporo Convention Center, Sapporo, Japan, October 2004.
- Samareh Samadi, **Ahmad Golmohammadi**, Abumoslem Jannesari, Mohammad Reza Movahedi, Babak Khalaj and Shahrokh Ghaemmaghani, "*A Novel Implementation of the IEEE802.11 Medium Access Control*" IEEE 2006 International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS 2006) Yonago Convention Center, Tottori, Japan.

Computer Skills

- **Developing Languages:** Visual C++, Pascal, Matlab, Assembly for C25 and C54x DSPs
- **Hardware Design Languages and Tools:** VHDL, AHDL, Xilinx Foundation, Altera Maxplus II, Leonardo Exemplar, Modelsim.
- **Packages:** Protel, TMS320C54x code composer.

Professional Membership

- 3 years IEEE Membership, since 1998.

References

- Are available upon request.